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PATENT
Customer No. 22,852
Attorney Docket No. 04329.3105

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Tsutomu TEZUKA, et al.)
) Group Art Unit: 2812
Application No.: 10/628,513)
) Examiner: Unknown
Filed: July 29, 2003)
)
For: SEMICONDUCTOR DEVICE)
MANUFACTURING METHOD)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicants bring to the attention of the Examiner the document listed on the attached PTO-1449. Applicants are filing this Supplemental Information Disclosure Statement (IDS) before the mailing date of a first Office Action on the merits for the above-referenced application, to the undersigned representative's knowledge at the time this paper was filed. This IDS supplements the IDSs filed on July 29 and December 23, 2003.

Applicants attach a copy of the listed document. Applicants respectfully request that the Examiner consider the listed document and indicate its consideration by making appropriate notation on the attached PTO-1449 Form.

Applicants point out that the listed document was previously submitted with the IDS filed on December 23, 2003, but was incompletely listed on the PTO-1449 form submitted with that

IDS. As a courtesy, Applicants have properly cited the document on the attached PTO-1449 form, and enclose another copy of the document for the Examiner.

This submission does not represent that a search has been made or that no better art exists, and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies the document as prior art against any claim in the application and Applicants determine that the listed document does not constitute "prior art" under United States law, Applicants reserves the right to present to the Office the relevant facts and law regarding the appropriate status of the document.

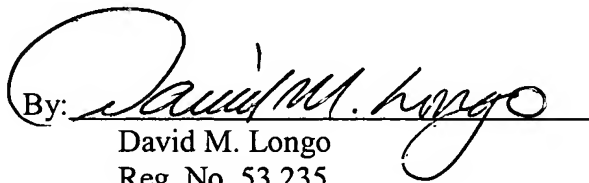
Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: October 7, 2004

By: 
David M. Longo
Reg. No. 53,235



INFORMATION DISCLOSURE CITATION

Atty. Docket No.	10/628,513	Appln. No.	10/628,513
Applicant	Tsutomu TEZUKA, et al.		
Filing Date	July 29, 2003	Group:	2812

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

T. Tezuka et al., "Dislocation-free formation of relaxed SiGe-on-insulator layers", American Institute of Physics, Applied Physics Letters, Vol. 80, No. 19, May 13, 2002, pp. 3560-3562

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce